

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1613	710/305.ccls	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 08:17
L2	104	(endian or swizzle or reorder or swapping or rearrange) and l1	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 08:52
L3	3440	(swizzl\$3 or swap\$4 or reorder\$2 or rearrange\$2) near2 (bit or byte)	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2005/12/02 09:14
L4	467	endian and l3	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 08:51
L5	267	interface same l3	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:32
L6	136	memory management and (l5 or l4)	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:01
L7	134	processor and l6	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:04
L8	61	memory management and l5	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:12
L9	60	processor and l8	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:12
L10	55	cache and l9	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:13
L11	30	(swizzl\$3 or swap\$4 or reorder\$2 or rearrange\$2) near2 (bit or byte) .ti	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2005/12/02 09:14
L12	0	memory management and l11	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:18
L13	27	interface near4 l3	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:31
L14	1	processor core same l5	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:31
L15	2	processor core and l5	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:32

L16	36	bus interface same I3	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:37
L17	1	bus interface near4 I3	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:32
L18	8305	virtual address	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2005/12/02 09:37
L19	17583	physical address	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2005/12/02 09:37
L20	822	memory management same I18 same I19	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:37
L21	628	translat\$4 same I20	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:52
L22	37	processor core and cache and I21	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 09:58
L23	133690	d near a converter	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:01
L24	51544	d a converter	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:00
L25	107807	a d converter	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:00
L26	2388	analog data same (I24 or I25)	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:01
L27	16	memory management and I26	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:03
L28	21856	(video or audio) same (I24 or I25)	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:02
L29	955	dsp same I28	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:04
L30	1	processor core and I29	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:03
L31	297	(I24 or I25) .ti.	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:06

L32	92893	processor .ti.	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:04
L33	2	I31 and I32	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:05
L34	0	I31 and I29	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:05
L35	385	dsp .ti.	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:05
L36	0	I31 and I35	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:05
L37	93248	(dsp or processor) .ti.	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:06
L38	40	I37 and I29	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2005/12/02 10:06